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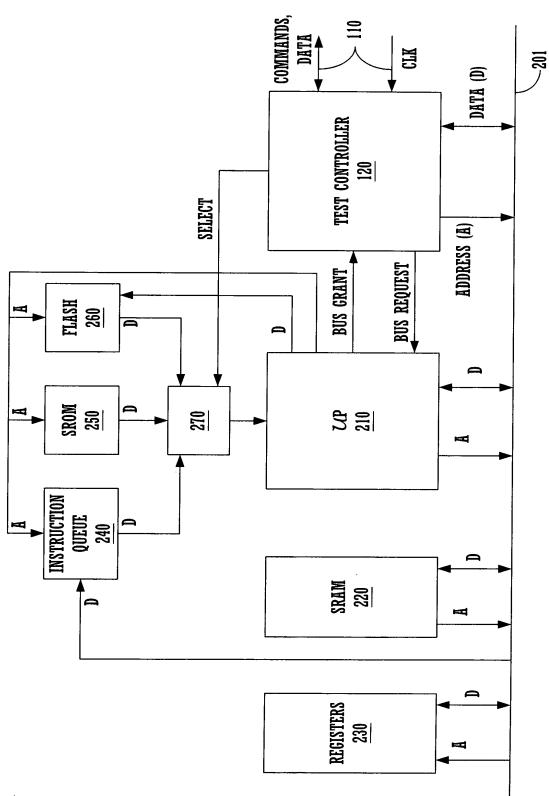
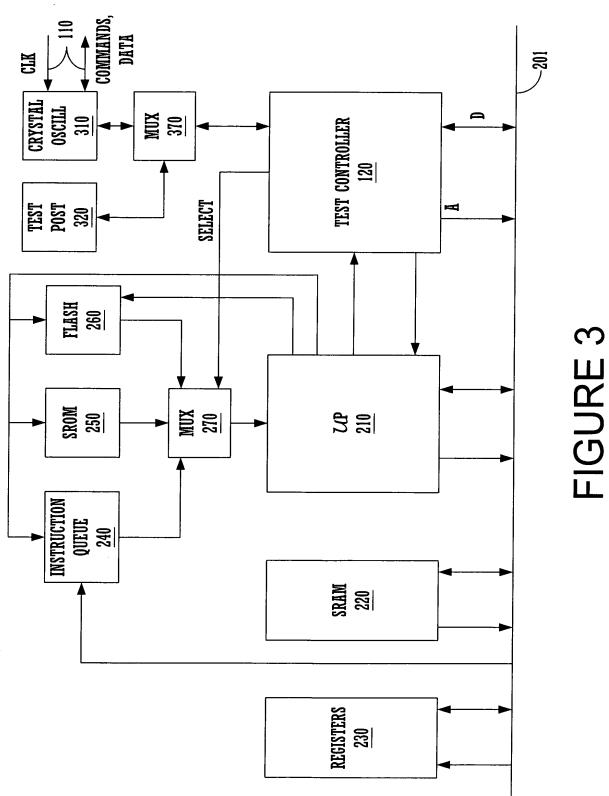


FIGURE 2



BEGIN TEST MODE IS ENTERED ESTABLISHING A MICROPROCESSOR AS A SLAVE AND 610 A TEST CONTROLLER AS A MASTER THE TEST CONTROLLER FILLS AN INSTRUCTION QUEUE WITH INSTRUCTIONS - 620 TO BE EXECUTED, THE INSTRUCTIONS ORIGINATE FROM A TEST INTERFACE THE MICROPROCESSOR IS FORCED TO EXECUTE INSTRUCTIONS FROM THE -630 INSTRUCTION QUEUE; THUS BYPASSING A PROGRAM FLASH COUPLED TO THE **MICROPROCESSOR** (OPTIONAL) THE TEST CONTROLLER TRANSFERS AN INSTRUCTION TO THE 640 INSTRUCTION QUEUE THAT CAUSES THE MICROPROCESSOR TO EXECUTE INSTRUCTIONS FROM A SUPERVISORY ROM INSTEAD OF THE INSTRUCTION QUEUE UPON COMPLETION OF A SET OF INSTRUCTIONS IN THE SUPERVISORY ROM. - 650 THE NEXT INSTRUCTION IN THE INSTRUCTION QUEUE IS EXECUTED EXECUTE FURTHER INSTRUCTIONS FROM THE INSTRUCTION QUEUE AND SROM -660UNTIL TEST MODE IS EXITED END

FIGURE 4

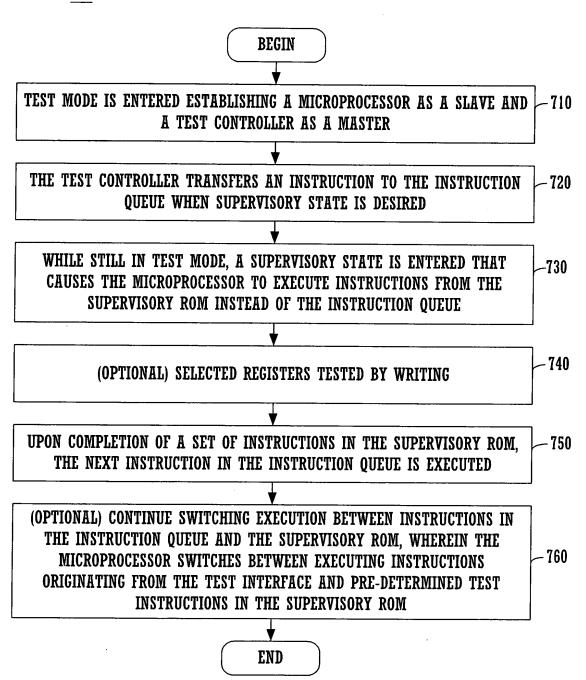


FIGURE 5

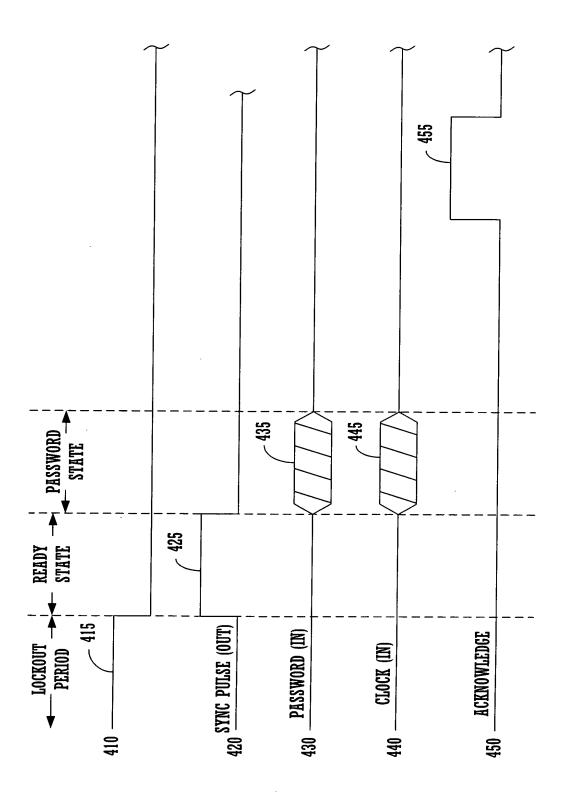


FIGURE 6

FIGURE 7

END